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APPLICANTS Vivek Nautiyal, Garhwal, INDIA; Ashish Kumar, Ranchi, INDIA;					
** CONTINUING DATA ***** <i>None JZ</i>					
** FOREIGN APPLICATIONS ***** INDIA 76/DEL/2003 01/30/2003 <i>JZ</i>					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/13/2004					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance <i>JZ</i>		STATE OR COUNTRY INDIA	SHEETS DRAWING 6	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 3
Verified and Acknowledged Examiner's Signature _____ Initials _____					
ADDRESS 23990					
TITLE Method and system for reducing power consumption in digital circuitry using charge redistribution circuits					
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		